

Acadia Optronics, LLC seeks Hardware Design Engineer Manager for Rockville, MD location.

Job Duties:

Oversee the design and development of FPGA-based offload engines. Author research proposals targeting SBIR & stimulus grants. Work with DOE funded labs and university scientists. Coordinate and direct projects. Train, manage, direct and evaluate the work of 1-2 Engineers. Manage the efficient and timely completion of projects for private and public sector clients. Negotiate contracts for outsourced technical functions, benchmark and execute purchase contracts for materials and required services. Confer with executives / management to ascertain project specifications and procedures.

Minimum Education and Experience Requirements: Master of Science degree in Computer Engineering.

Special Skills: Employer will accept any suitable combination of education, training and experience. Applicants must also possess knowledge which may be gained concurrently through coursework or experience with: FPGA based hardware using Verilog and VHDL, Industry standard FPGA and ASIC design tools such as Xilinx ISE, ModelSim, Aldec, Synopsys and Cryptographic algorithms, DSP functions, and network stack processing blocks in hardware.

Qualified applicants submit resume to recruiting@acadiaoptronics.com and list Job Code 091.